APPLICATION

★ PH015TN1802A

1.5" TFT LCD Module

2019/07/01

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	1.5 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	$320 \times 3(RGB) \times 240$	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	$0.1035(W) \times 0.1060(H)$ mm	
6	Active area	$33.12(W) \times 25.44(H)$ mm	
7	Module size	$32.72(W) \times 36.90(H) \times 2.0(D)$ mm	
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	SYNC Mode	
11	View direction(Gray inversion)	12 o'clock	

部材图

翼龙正和国际有限公司

MODEL NO. 背光号	触摸
MATERIAL 材料	
TYPE 类型	
PART NO. 锦样料号	PH015TN1802A

0.5*39=19.50±0.05

3.50

0.30

1

40

2.00±0.2

0.30±0.05

1.89

36.90±0.2(LCM O/D)

33.12(LCD AA)

1.5 Inch
320*RGB*240

40

20.50±0.05

1

4.00

1.90

25.44(LCD AA)

32.72±0.2(LCM O/D)

23.14±0.5
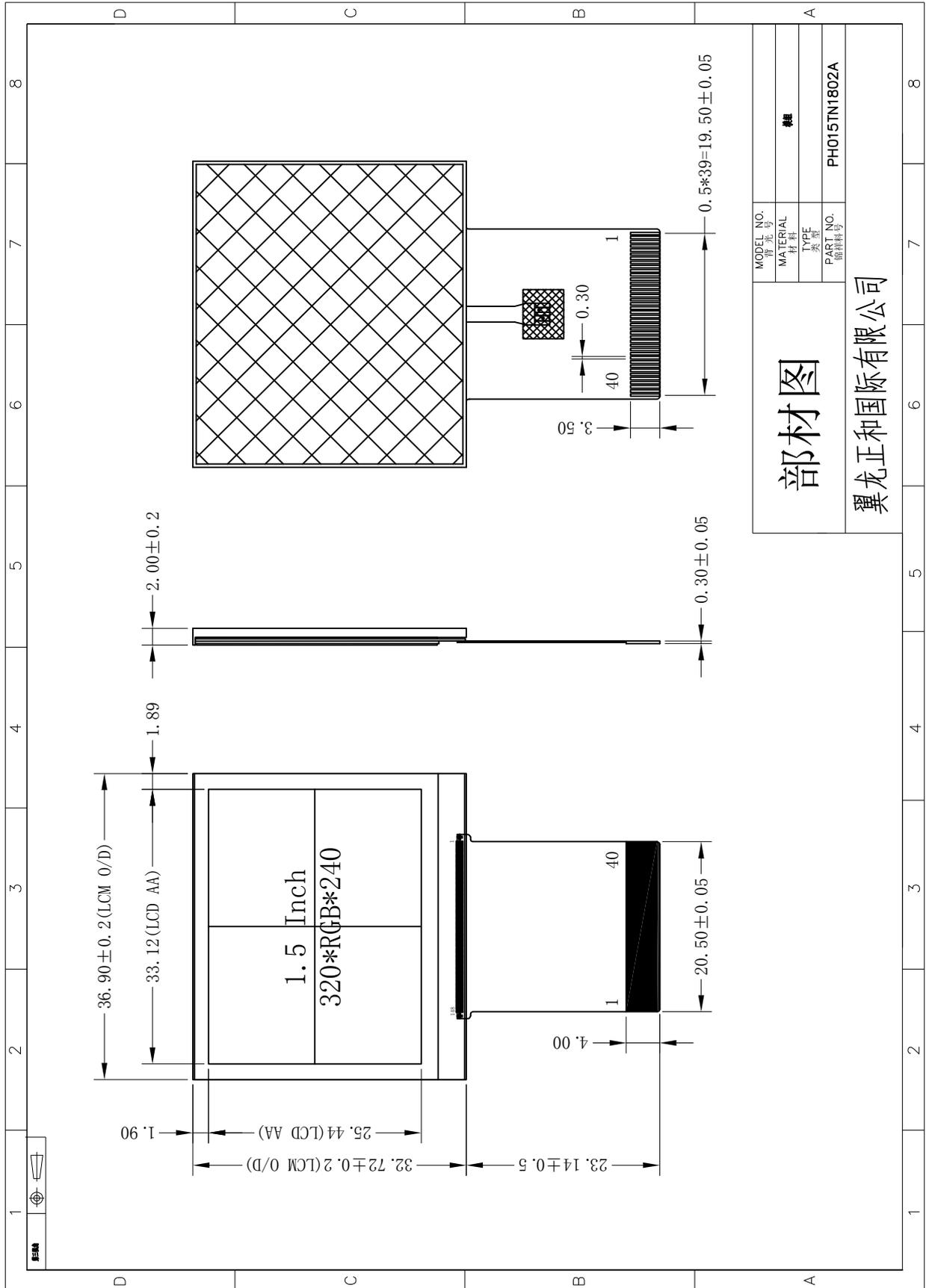
2. Pin Assignment

FPC Connector is used for the module electronics interface. The recommended model is FH19SC-40S-0.5SH manufactured by HIROSE.

Pin	Symbol	I/O	Description	Remark
1	DUMMY	--	Dummy	
2	DUMMY	--	Dummy	
3	DUMMY	--	Dummy	
4	DUMMY	--	Dummy	
5	DUMMY	--	Dummy	
6	DUMMY	--	Dummy	
7	DUMMY	--	Dummy	
8	DUMMY	--	Dummy	
9	DUMMY	--	Dummy	
10	VCOML	C	Capacitor for VCOM low	
11	VCOMH	C	Capacitor for VCOM high	
12	AGND	--	Analog ground	
13	DUMMY	--	Dummy	
14	AVDD	C	Regulation capacitor for analog voltage	
15	DUMMY	--	Dummy	
16	DUMMY	--	Dummy	
17	DUMMY	--	Dummy verter	
18	DUMMY	--	Dummy	
19	LED-	--	LED power: cathode	**Note 1**
20	DUMMY	--	Dummy	
21	DUMMY	--	Dummy	
22	LED+	--	LED power: anode	**Note 1**
23	GND	--	Ground	
24	VCC	--	Power supply for digital circuit and charge pump circuit	
25	VSYNC	I	Vertical sync input. Negative polarity	
26	HSYNC	I	Horizontal sync input. Negative polarity	
27	DCLK	I	Clock signal, latch data onto line latches at the rising edge	
28	GND	--	ground	
29	GND	--	ground	
30	DIN0	I	Data input	

31	DIN1	I	Data input	
32	DIN2	I	Data input	
33	DIN3	I	Data input	
34	DIN4	I	Data input	
35	DIN6	I	Data input	
36	SDA	I/O	Serial interface data line	
37	SCL	I	Serial interface clock line	
38	SCEN	I	Serial interface chip enable line	
39	SHDB	I	Shutdown input	
40	GREST	I	System reset pin	