

APPLICATION

★ PH032BN2505B V.6

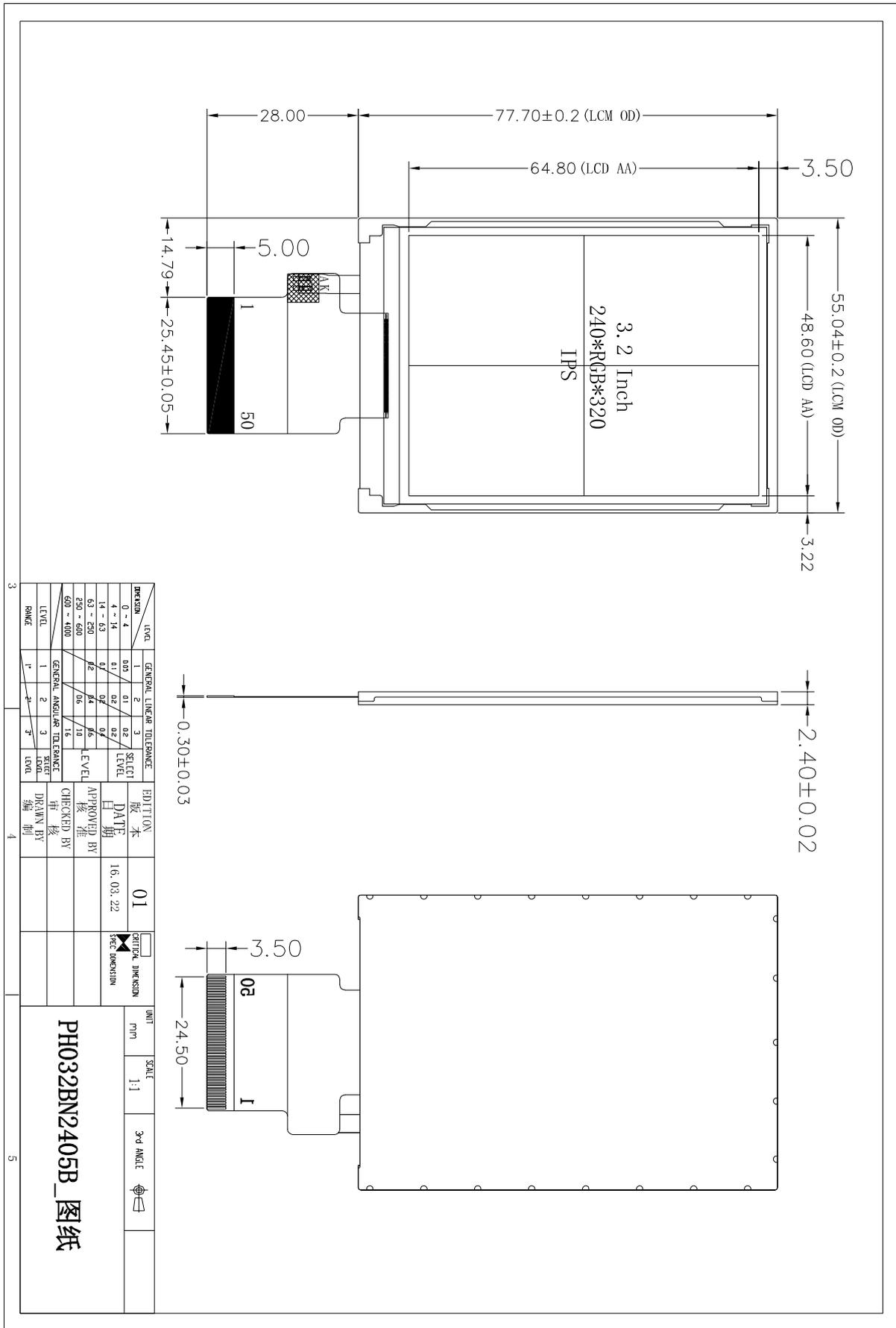
3.2" TFT LCD Module

2022/06/23

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	3.2 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	240 × 3(RGB) × 320	
4	Display mode	Normally Black	
5	Dot pitch	0.1461(W) × 0.1461(H) mm	
6	Active area	48.60(W) × 64.80(H) mm	
7	Module size	55.04(W) × 77.70(H) × 2.35(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB Vertical Stripe	
10	Interface	MCU 16 Mode	
11	View direction(Gray inversion)	IPS	

3. Dimensional Outline



2. Pin Assignment

Pin No.	Symbol	I/O	Description	Note
1	NC	I/O	NC	
2	NC	I/O	NC	
3	NC	I/O	NC	
4	NC	I/O	NC	
5	NC	I/O	NC	
6	NC	I/O	NC	
7	NC	I/O	NC	
8	NC	I/O	NC	
9	NC	I/O	NC	
10	NC	I/O	NC	
11	TE	I/O	Tearing effect signal is used to synchronize MCU to frame memory	
12	DB15	I	LCD Data bus	
13	DB14	I	LCD Data bus	
14	DB13	I	LCD Data bus	
15	DB12	I	LCD Data bus	
16	DB11	I	LCD Data bus	
17	DB10	I	LCD Data bus	
18	DB09	I	LCD Data bus	
19	DB08	I	LCD Data bus	
20	DB07	I	LCD Data bus	
21	DB06	I	LCD Data bus	
22	DB05	I	LCD Data bus	
23	DB04	I	LCD Data bus	

24	DB03	I/O	LCD Data bus	
25	DB02	I/O	LCD Data bus	
26	DB01	I/O	LCD Data bus	
27	DB00	I/O	LCD Data bus	
28	GND	I/O	Ground	
29	RD	I/O	Read execution control pin	
30	WR	I/O	Write execution control pin	
31	RS	I/O	Display data / Command selection pin D/CX='1': Display data. D/CX='0': Command data.	
32	CS	I/O	Chip Selection	
33	RESET	P	RESET	
34	VCI	P	Power supply	
35	GND	P	Ground	
36	VCI	I	Power supply	
37	NC	P	NC	
38	NC	I/O	NC	
39	NC	I/O	NC	
40	NC	I/O	NC	
41	NC	I/O	NC	
42	NC	I/O	NC	
43	NC	I/O	NC	
44	NC	I/O	NC	
45	NC	I/O	NC	
46	NC	P	NC	
47	NC	P	NC	
48	NC	P	NC	
49	LED_A	I	LED_A	
50	LED_K	P	LED_K	